



AMENDMENT UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
GROUP 2811
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q60098

Yoko HORIGUCHI

Appln. No.: 09/615,705

Group Art Unit: 2811

Confirmation No.: 4487

Examiner: O. Nadav

Filed: July 13, 2000

For: SEMICONDUCTOR INTEGRATED CIRCUIT

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AMENDMENT UNDER 37 C.F.R. § 1.116

ATTN: BOX AF

Commissioner for Patents
Washington, D.C. 20231

Sir:

A Petition for an Extension of Time of One Month, along with the required fee, is being concurrently filed with this Amendment.

In response to the Office Action dated August 23, 2002, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Page 6, please delete the second full paragraph, and replace it with the following new paragraph:

Similarly, in the semiconductor integrated circuit device 201', the discharge speed of the charge charged in the ground potential wire 202 from the input/output terminal 212 through the electrostatic protection element 214 is slower than the discharge speed of the charge charged in the

dis not
O.N.